



2121
94/19W

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Hoppensteadt et al. :
Serial No.: 09/771,019 : Examiner: Hirl, Joseph P.
Filed: January 26, 2001 : Group Art Unit: 2121
TITLE: **Phase-locked Loop Oscillatory Neurocomputer**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATE OF MAILING BY FIRST CLASS MAIL 1.8

I hereby certify that the following documents for the above-identified patent application are being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed as shown above, on the date shown below.

1. Letter Re Missing Drawing Sheet (1 page);
2. Sheet 3 of the Drawings (1 page); and
3. A return receipt postcard.

Dated: 6/6/05

By: Suzie Shields
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LETTER RE MISSING DRAWING SHEET

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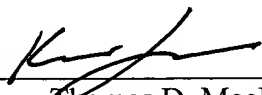
Dear Sir:

Per the request of the examiner in charge of this application in a telephone conference of June 6, 2005 with the undersigned attorney for applicant, enclosed is a copy of sheet 3 of the drawings in the above application to replace that missing from the Patent and Trademark Office file.

Respectfully submitted,

GALLAGHER & KENNEDY, P.A.

Date: June 6, 2005

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